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04/07/2004

LATTICE SEMICONDUCTOR CORPORATION
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Mark Backer	(Depositor's name)
Mark Backer	(Signature)
4/12/04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/082,050	02/14/2002	Stephen R. Cebenko	1-1-3-1-1	9454

TITLE OF INVENTION: INTEGRATED CIRCUIT BASE TRANSISTOR STRUCTURE AND ASSOCIATED PROGRAMMABLE CELL LIBRARY

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	07/07/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
NGUYEN, DAO H	2818	257-292000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.563).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Lattice Semiconductor Corporation

Hillsboro, Oregon

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

☒ Issue Fee

☒ Publication Fee

☐ Advance Order - # of Copies

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